

Development of a New Lightweight Encryption Algorithm

Ardabek Khompysh¹, Nursulu Kapalova², Oleg Lizunov^{3*}, Dilmukhanbet Dyusenbayev⁴, Kairat Sakan⁵
Information Security Laboratory, Institute of Information and Computational Technologies, Almaty, Kazakhstan^{1,2,3,4,5}
Department of Information Technology, Al-Farabi Kazakh National University, Almaty, Kazakhstan^{1,5}

Abstract—Due to the growing need to use devices with low hardware resources in everyday life, the likelihood of their susceptibility to various cyber-attacks increases. In this regard, one of the methods to ensure the security of information circulating in these devices is encryption. For devices with small hardware resources, the most applicable is low-resource (lightweight) cryptography. This article introduces a new lightweight encryption algorithm, ISL-LWS (Information Security Laboratory – lightweight system), designed to protect data on resource-constrained devices. The encryption algorithm is implemented in the C++ programming language. The paper presents the statistical properties of ciphertexts obtained using the developed algorithm. For the experimental testing for statistical security, the sets of statistical tests by NIST and D. Knuth were used. Separately, the ISL-LWS algorithm was tested for avalanche effect properties. The obtained results of statistical tests were compared with the Present and Speck modern lightweight algorithms. The study and comparative analysis of the speed of encryption and key generation of the three algorithms were carried out on the Arduino Uno R3 board.

Keywords—Lightweight block cipher; S-box; linear transformation; avalanche effect; IoT devices; RFID tags; null hypothesis; NIST tests; D. knuth tests

I. INTRODUCTION

The main directions of the development of cryptography are largely associated with the development of communications and information technology. It is the progress in these areas that has made possible the widespread use of compact devices with low computing power that have access to the Internet and implement the concept of the Internet of Things (IoT) [1][2]. Examples of such devices are radio frequency tags (RFID), automated process control systems (SCADA), wireless sensors, electronic personal identification tools, etc. [3].

Lightweight ciphers are often less secure than traditional ciphers such as AES. This is because lightweight ciphers are optimized for high speed and low power consumption, not maximum security.

As defined by the US National Institute of Standards and Technology (NIST), lightweight cryptography is a subcategory of cryptography that aims to provide solutions for high-growth applications that make extensive use of low-power smart devices [4][5]. Modern cryptographic algorithms can work well on computers, servers, and some mobile phones, but IoT devices, smart cards, and RFID tags require the use of lightweight cryptographic algorithms [6].

When building lightweight block encryption algorithms, the following architectural solutions are used [7]:

- Reduction of the block size from 128 bits to 64 bits;
- Use of keys 64, 80, and 128 bits long;
- Use of 4-bit S-boxes instead of 8-bit ones;
- Use of a simplified key schedule.

Designing algorithms based on well-studied and widely used operations that perform elementary linear/nonlinear transformations.

When creating lightweight block ciphers, the following structures are used [8]:

- Feistel network;
- Substitution-permutation network (SP-network) using substitution boxes of small length;
- LRX-structure (logical operations, rotate left (right) shift, and addition modulo 2);
- ARX-structure (addition modulo 2^n , rotate left (right) shift, and addition modulo 2).

One of the main issues in lightweight cryptography is achieving a balance between security, efficiency, and cost. Obviously, optimizing a lightweight cipher to achieve high speed can weaken some of its security properties, and the algorithm will be more vulnerable to some attacks. Therefore, when developing a lightweight cipher, the first step is to determine the requirements for its security and limited resources, taking into account the scope of its application. When developing the encryption algorithm, the authors tried to balance security and speed.

This article presents a new lightweight symmetric block cipher algorithm ISL-LWS and its statistical analysis. The scientific novelty of the proposed algorithm is the SP transformation, which is performed in parallel by linear (P-box) and non-linear (S-box) cryptographic primitives, where two S-boxes are used simultaneously. This procedure makes it possible to increase the degree of non-linearity and data confusion in fewer rounds. An overview of related work is presented in the next Section II. Section III presents the developed algorithm, which is designed according to the Feistel network and includes linear and non-linear transformations that provide a high level of diffusion and confusion. The round key schedule algorithm is also presented

here. The results and discussion of the statistical tests are presented in Section IV. In addition, this section describes data on the hardware-software implementation of the algorithm and comparative performance analysis. Section V presents the conclusion, where the results of the work are indicated.

II. RELATED WORK

To date, a fairly large number of lightweight block encryption algorithms based on SP networks and Feistel networks are known [9]. Both approaches have their advantages and disadvantages in the context of constructing algorithms in conditions of limited resources. Lightweight block ciphers are represented by the following algorithms: Present [10][11], Clefia [12], Katan [13], Simon [14], Speck [15], Secure IoT (SIT) [16], etc.

A study by Xinxin Fan et al. (Fan et al. 2013) introduced a lightweight WG-8 encryption algorithm of the Welch-Gong family of stream ciphers, adapted for devices with low hardware resources [17]. Typically, some of them have been improved and developed by simplifying block ciphers to improve their performance. For example, DESL which is also known as lightweight DES, is a variant of classic DES. The main difference between the DESL cipher and the DES algorithm is that the former uses one S-box instead of eight ones, which reduces the ROM requirements for storing tables by eight times.

The lightweight encryption algorithm Present [18] is described in the article by L.K. Babenko, D.A. Bespalov, O.B. Makarevich, R.D. Chesnokov, and Ya.A. Trubnikov. The authors of this article have developed a software implementation and synthesized it into a hardware unit for a system on a chip within the framework of the requirements for low-resource cryptography, having obtained a sufficiently effective solution for its application in devices. In 2012, the ISO and IEC organizations included the Present algorithm in the international standard for lightweight encryption ISO/IEC 29192-2:2012.

Speck is a block lightweight encryption algorithm developed by the US National Security Agency. Speck is one of the fastest in lightweight cipher benchmarks, but its performance is highly dependent on architecture. Speck supports several block and key sizes. The block length can be 32, 48, 64, 96, and 128 bits. The key length depends on the block size. The range of key sizes is 64, 72, 96, 128, 144, 192, and 256 bits. The number of encryption rounds depends on the block size and the key. The range of rounds is 22, 23, 26, 27, 28, 29, 32, 33, and 34. Speck is standardized by ISO within the RFID air interface standard [15].

In a study by Muhammad Usman et al. 64-bit block lightweight encryption algorithm SIT [16] with a key length of 64 bits is considered. The architecture of the algorithm is a mixture of a Feistel network and an SP network. Conducted studies show that the algorithm provides significant security after five rounds of encryption.

Thus, R&D on the development and study of lightweight encryption algorithms is relevant.

III. LIGHTWEIGHT ENCRYPTION ALGORITHM ISL-LWC

The block diagram of the proposed ISL-LWC lightweight block encryption algorithm is shown in Fig. 1.

The main parameters of the algorithm:

- block length – 64 bits;
- key length – 80 bits;
- number of encryption rounds - 16.

The algorithm uses SP transformation, modulo 2 addition (XOR operation), rotate shift, and non-linear transformations in the form of S-boxes (S).

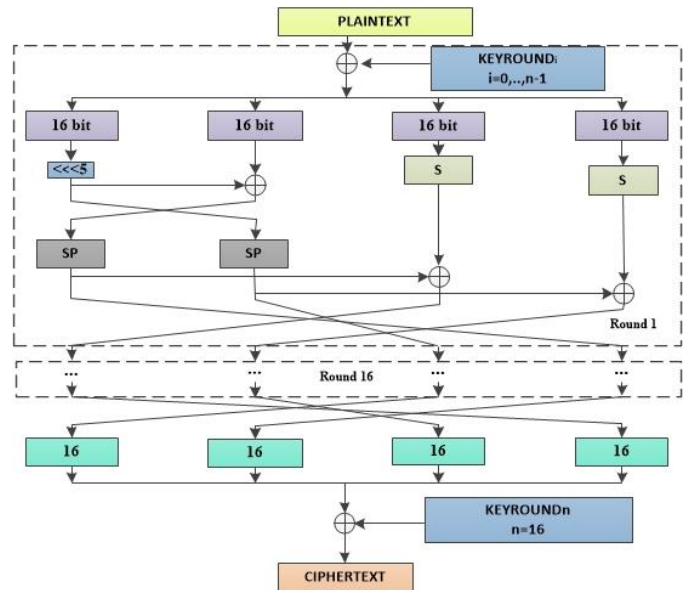


Fig. 1 Scheme of the encryption algorithm.

The encryption process consists of 4 stages:

Stage 1. A 64-bit plaintext block is added to the round key modulo 2 (XOR operation). Next, the resulting 64-bit block is divided into 4 subblocks of 16 bits each (the subblocks are numbered from left to right).

Stage 2. The 1st input subblock is rotated by 5, then the obtained value of the 1st input block is summed (XOR operation) with the 2nd subblock, and the resulting values are swapped in accordance with the scheme and go through SP transformations.

Stage 3. The 3rd and 4th sub-blocks go through the transformation S and then are added (XOR operation) with the results obtained at Stage 2 according to the scheme.

Stage 4. The results of Stages 2 and 3 are swapped according to the scheme of the encryption algorithm.

1) *SP transformation*: The SP transformation (Fig. 2) consists of non-linear 4-bit substitutions S-box1 and S-box2 (Tables I, II) and a linear bit permutation P-box (Table III). The methods for obtaining S-box1 and S-box2 are shown in [19]. The transformations above make it possible to perform confusion and diffusion.

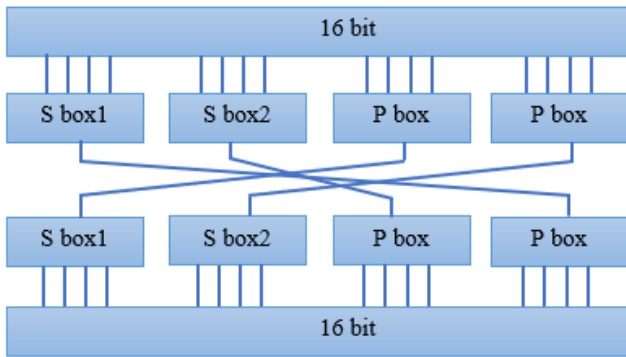


Fig. 2 SP transformation scheme.

TABLE I S-BOX1 SUBSTITUTION

0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
2	E	D	6	8	A	B	1	5	3	4	9	0	F	7	C

TABLE II S-BOX2 SUBSTITUTION

0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
F	5	D	8	C	2	4	7	0	9	6	A	1	3	E	B

TABLE III P-BOX BIT PERMUTATION

i	0	1	2	3
P(i)	3	2	0	1

2) *S transformation*; Input 16 bits are represented as $a_0a_1a_2a_3a_4a_5a_6a_7a_8a_9a_{10}a_{11}a_{12}a_{13}a_{14}a_{15}$ of which every sequential 4 bits are represented as $m_i, i = 0, 3$. $m_0 = a_0a_1a_2a_3$, $m_1 = a_4a_5a_6a_7$, $m_2 = a_8a_9a_{10}a_{11}$, $m_3 = a_{12}a_{13}a_{14}a_{15}$. The values m_0, m_2 and m_1, m_3 are passed through 4-bit S-box1 and S-box2 and then swapped according to Fig. 3.

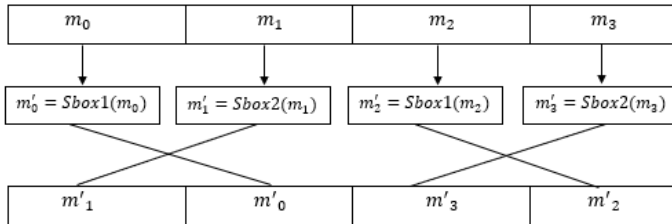


Fig. 3 S-box transformation process.

Round subkeys are generated on the basis of an 80-bit base key, which is divided into five sub-blocks of 16 bits each (sub-blocks are numbered from left to right) (Fig. 4). The cryptographic transformations used are the 4-bit S-box and addition modulo 2 raised to the power of the word length.

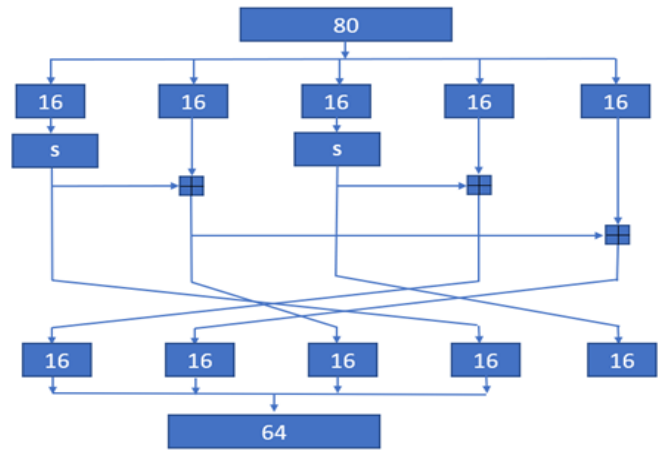


Fig. 4 Algorithm for generating round keys.

IV. STATISTICAL ANALYSIS OF CIPHERTEXTS

One of the main ways to test a block encryption algorithm for security is to conduct statistical analysis since most cryptographic attacks are based on the search for statistical vulnerabilities in the ciphertext.

To test sequences for randomness, there are a large number of algorithms, and for the convenience of checking sequences, software products have already been implemented that contain some sets of tests. Among them, the most common are the tests proposed by NIST, DIEHARD, CRYPT-X, D. Knuth, and others [20].

For statistical analysis of ciphertexts obtained using the ISL-LWC, Present, and Speck encryption algorithms, the NIST and D. Knuth test sets were used.

1) *NIST statistical tests*: NIST has developed a number of statistical tests which are based on the task of calculating a statistic that characterizes a certain property of a sequence compared with a reference statistic. Reference statistics are obtained mathematically, which is the subject of many theorems and scientific papers on cryptography, probability theory, and number theory. NIST tests have already been used to study the output sequences of cryptographic systems [21]. The tests are based on the concept of the null hypothesis. The null hypothesis is the assumption that there is some relationship between the occurrence of numbers. In other words, the null hypothesis is the assumption that the sequence is truly random (the symbols of which appear equally likely and independently of each other). Therefore, if such a hypothesis is true, then the encryption algorithm will perform well statistically.

To obtain the results of testing ciphers 15 NIST statistical tests were used: frequency bit test, frequency block test, test for a sequence of identical bits, test for the longest sequence of ones in a block, test for binary matrix ranks, spectral test, test for matching non-overlapping patterns, overlapping pattern matching test, Maurer's universal statistical test, linear complexity test, periodicity test, approximate entropy test, cumulative sums test, arbitrary variance test, and another arbitrary variance test [22].

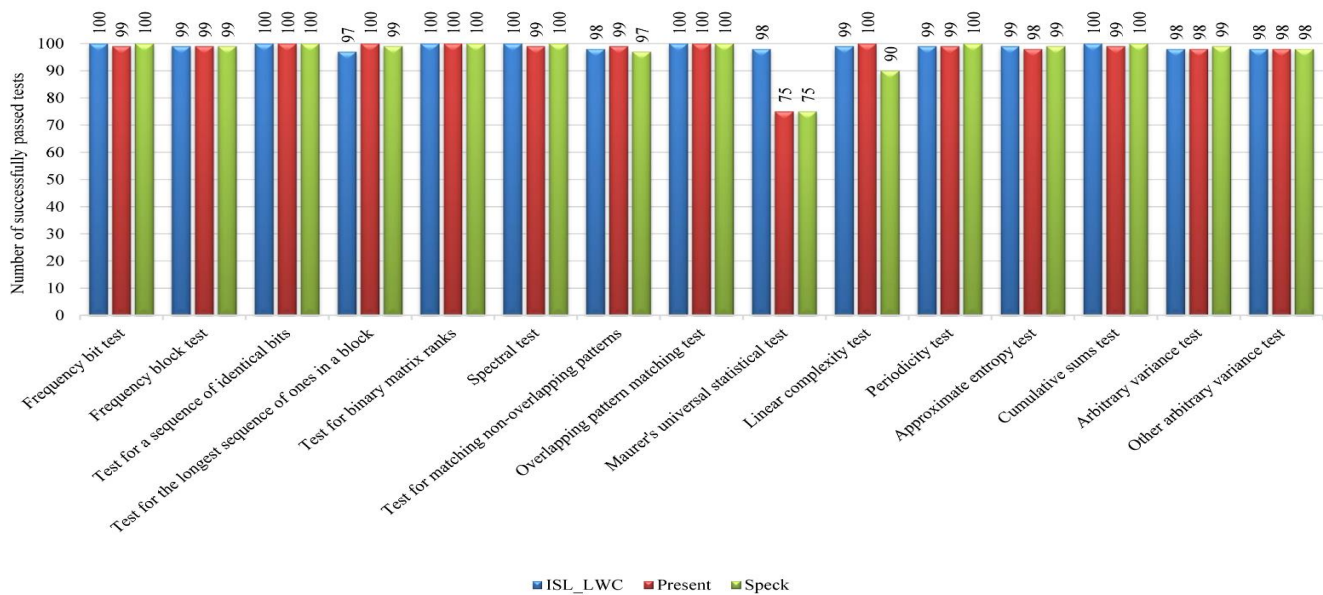


Fig. 5 Comparative analysis of successfully passed NIST tests.

To study the statistical security of the ISL-LWC, Present, and Speck encryption algorithms using NIST tests, each algorithm encrypted 20 files, differing in size, on five different keys. As a result, 100 files were encrypted with each algorithm. The number of successfully passed NIST tests and a comparative analysis of the ISL-LWC, Present, and Speck encryption algorithms are shown in Fig. 5.

In each test, a so-called P-value is calculated, which indicates the level of randomness. If the P-value = 1, then the sequence is perfectly random, and if it is zero, then the sequence is completely predictable. Next, the P-value is compared with the threshold level of randomness α , and if it is greater than α , then the null hypothesis is accepted and the sequence is recognized as random, otherwise, it is recognized as non-random.

In the tests, $\alpha = 0.01$ is assumed. Therefore:

- If the P-value ≥ 0.01 , then the sequence is considered random with a confidence level of 99%;
- If the P-value < 0.01 , then the sequence is considered non-random with a confidence level of 99%.

As a result of the study and comparative analysis of the three encryption algorithms according to NIST tests, it was found that the percentage of successfully passed tests by algorithms is: ISL-LWC – 99%, Present – 97.5%, Speck – 97%. From the obtained results, we can conclude that the ISL-LWC algorithm satisfies the statistical security criteria.

2) *Statistical tests by D. Knuth*: One of the first sets of statistical tests was proposed by D. Knuth in 1969 and described in his classic work "The Art of Computer Programming". D. Knuth's set contains such tests as the serial test, gap test, poker test, coupon collector test, permutation test, monotonicity test, and correlation test. The tests are based on the chi-square (χ^2) statistical test. The calculated value of the χ^2 statistic is compared with the tabular results and, depending on the probability of occurrence of such a statistic, a conclusion is made about its quality [23]. Among the advantages of these tests are their small number and the existence of fast execution algorithms. The disadvantage is the uncertainty in the interpretation of the results [24].

To study the statistical security of the ISL-LWC, Present, and Speck encryption algorithms using the D. Knuth tests, we encrypted with each algorithm the same 100 files that were checked using the NIST tests. The number of successfully passed tests by D. Knuth and a comparative analysis of the ISL-LWC, Present, and Speck encryption algorithms are shown in Fig. 6.

As a result of the study on the tests of D. Knuth and a comparative analysis of the three encryption algorithms, it was found that the percentage of successfully passed tests by the algorithms is 93.5% for ISL-LWC, 99% for Present, and 99% for Speck. From the obtained results, we can conclude that the ISL-LWC algorithm satisfies the statistical security criteria.

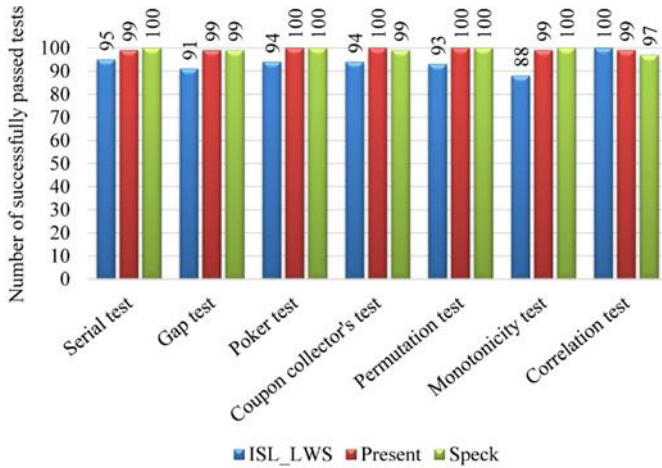


Fig. 6 Comparative analysis of successfully passed tests by D. Knuth.

3) *Study of statistical security indicators:* For the study of statistical security, the following indicators were considered:

- average number of output bits that change when one input bit changes (avalanche effect);
- degree of completeness (d_c);
- degree of avalanche effect (d_a);
- degree of strict avalanche criterion (d_{sa}).

They are considered for various numbers of cycles and randomly taken encryption keys. The definition of the above indicators is presented in [25].

The essence of the experiment is to evaluate the depth of the avalanche effect of the ISL-LWC encryption algorithm, which is determined by the number of encryption rounds. The experiment was carried out on 100, 1000, and 10,000 blocks of ciphertext obtained using the ISL-LWC algorithm. Table IV presents the results of the assessment of the statistical security indicators of the ISL-LWC cipher.

TABLE IV RESULTS OF THE ASSESSMENT OF STATISTICAL SECURITY INDICATORS OF THE ISL-LWC CIPHER

Round number	D_{min}	D_{max}	d_w	M_{min}	M_{max}	m_w	d_c	d_a	d_{sa}
ISL-LWC (100 blocks)									
1	9.00 20	10.8 636	9.93 28	3.074 85	5.70 76	4.3912	0.125	0.1372	0.0867
2	50.6 934	52.5 550	51.6 242	11.30 266	13.9 354	12.61906	0.4682	0.3943	0.3314
3	64.6 894	66.5 510	65.6 202	20.81 73	23.4 501	22.1337	0.8593	0.6916	0.6345
4	43.9 609	45.8 226	44.8 918	27.31 95	29.9 523	28.6359	0.9843	0.8939	0.8350
5	20.9 659	22.8 276	21.8 968	30.14 76	32.7 804	31.4640	1	0.9776	0.9114
6	15.5 450	17.4 067	16.4 759	30.57 96	33.2 124	31.8960	1	0.9898	0.9194
7	14.8 608	16.7 225	15.7 916	30.74 11	33.3 739	32.0575	1	0.9887	0.9219
8	14.4	16.2	15.3	30.62	33.2	31.9418	1	0.9894	0.9203

Round number	D_{min}	D_{max}	d_w	M_{min}	M_{max}	m_w	d_c	d_a	d_{sa}
	042	658	350	54	582				
9	14.7 902	16.6 519	15.7 211	30.58 53	33.2 181	31.9017	1	0.9905	0.9176
10	15.1 740	17.0 357	16.1 049	30.61 01	33.2 429	31.9265	1	0.9902	0.9218
11	15.4 715	17.3 332	16.4 024	30.67 45	33.3 073	31.9909	1	0.9911	0.9196
12	15.1 822	17.0 439	16.1 131	30.73 21	33.3 649	32.0485	1	0.9926	0.9216
13	14.9 554	16.8 170	15.8 862	30.66 18	33.2 946	31.9782	1	0.9903	0.9211
14	15.0 505	16.9 122	15.9 813	30.59 75	33.2 003	31.8839	1	0.989086	0.9199
15	14.6 191	16.4 812	15.5 504	30.62 61	33.2 589	31.9425	1	0.9898	0.9224
16	14.8 984	16.7 601	15.8 292	30.64 67	33.2 795	31.9631	1	0.9893	0.9201
ISL-LWC (1000 blocks)									
1	9,00 20	10,8 637	9,93 29	3,074 9	5,70 77	4,3913	0,1250	0,1372	0,0867
2	50,6 934	52,5 551	51,6 243	11,3 027	13,9 355	12,6	0,46	0,39	0,33
3	64,6 894	66,5 511	65,6 202	20,8 174	23,4 502	22,1	0,85	0,69	0,63
4	43,9 610	45,8 227	44,8 918	27,3 195	29,9 523	28,6	0,98	0,89	0,83
5	20,9 660	22,8 277	21,8 968	30,1 477	32,7 805	31,4	1	0,97	0,91
6	15,5 451	17,4 068	16,4 759	30,5 797	33,2 125	31,8	1	0,98	0,91
7	14,8 609	16,7 225	15,7 917	30,7 411	33,3 739	32,0	1	0,98	0,92
8	14,4 042	16,2 659	15,3 351	30,6 255	33,2 583	31,9	1	0,98	0,92
9	14,7 903	16,6 520	15,7 211	30,5 853	33,2 181	31,9	1	0,99	0,91
10	15,1 741	17,0 358	16,1 049	30,6 102	33,2 430	31,9	1	0,99	0,92
11	15,4 716	17,3 333	16,4 024	30,6 745	33,3 073	31,9	1	0,99	0,91
12	15,1 823	17,0 439	16,1 131	30,7 322	33,3 650	32,0	1	0,99	0,92
13	14,9 554	16,8 171	15,8 862	30,6 619	33,2 947	31,9	1	0,99	0,92
14	15,0 505	16,9 122	15,9 814	30,5 975	33,2 003	31,8	1	0,98	0,92
15	14,6 191	16,4 813	15,5 504	30,6 261	33,2 589	31,9	1	0,98	0,92
16	14,8 984	16,7 601	15,8 293	30,6 467	33,2 795	31,9	1	0,98	0,92
ISL-LWC (10000 blocks)									
1	9,00 20	10,8 637	9,93 29	3,07 49	5,70 77	4,39	0,12	0,13	0,08
2	50,6 934	52,5 551	51,6 243	11,3 027	13,9 355	12,6	0,46	0,39	0,33
3	64,6 894	66,5 511	65,6 202	20,8 174	23,4 502	22,1	0,85	0,69	0,63
4	43,9 610	45,8 227	44,8 918	27,3 195	29,9 523	28,6	0,98	0,89	0,83
5	20,9 660	22,8 277	21,8 968	30,1 477	32,7 805	31,4	1	0,97	0,91
6	15,5 451	17,4 068	16,4 759	30,5 797	33,2 125	31,8	1	0,98	0,91

Round number	D_{min}	D_{max}	d_w	M_{min}	M_{max}	m_w	d_c	d_a	d_{sa}
7	14,8609	16,7225	15,7917	30,7411	33,3739	32,0575	1	0,9888	0,9219
8	14,4042	16,2659	15,3351	30,6255	33,2583	31,9419	1	0,9895	0,9204
9	14,7903	16,6520	15,7211	30,5853	33,2181	31,9017	1	0,9906	0,9177
10	15,1741	17,0358	16,1049	30,6102	33,2430	31,9266	1	0,9902	0,9218
11	15,4716	17,3333	16,4024	30,6745	33,3073	31,9909	1	0,9911	0,9197
12	15,1823	17,0439	16,1131	30,7322	33,3650	32,0486	1	0,9927	0,9217
13	14,9554	16,8171	15,8862	30,6619	33,2947	31,9783	1	0,9904	0,9212
14	15,0505	16,9122	15,9814	30,5975	33,2003	31,8839	1	0,9891	0,9200
15	14,6191	16,4813	15,5504	30,6261	33,2589	31,9425	1	0,9899	0,9225
16	14,8984	16,7601	15,8293	30,6467	33,2795	31,9631	1	0,9894	0,9202

In Table IV, the following designations are used:

- M_{min} is the minimum value of the mathematical expectation of the number of changed bits for some bit at the input;
- M_{max} is the maximum value of the mathematical expectation of the number of changed bits for some bit at the input;
- D_{min} and D_{max} are the variances of the number of changed bits in the bitwise estimation of the minima and maxima of the mean values;

m_w is the average number of changed bits:

$$m_w = \frac{M_{min} + M_{max}}{2} \quad (1)$$

Analyzing the data obtained in Table IV, we can conclude that with an increase in the number of blocks for encryption, more accurate values of d_a and d_{sa} , are obtained, i.e. they approach value 1 faster in the fourth and subsequent rounds. As a result of the study, it was found that at the 4th round of encryption of the ISL-LWC algorithm, the input sequence is completely confused.

Results of the study and comparative analysis of the time of encryption and key generation on the Arduino Uno R3 board.

Encryption time testing for three encryption algorithms Speck, Present, and ISL-LWC was carried out on the Arduino Uno R3 board (Fig. 7).

- main features of Arduino Uno R3;
- microcontroller - ATmega328;
- clock frequency - 16 MHz;
- operating voltage - 5 V;
- flash memory - 32 MB;
- RAM - 2 Kb.



Fig. 7 Arduino Uno R3 board.

In [25] Arduino IDE version 2.0.0-rc3 was used to compile and upload the source code of lightweight encryption algorithms to the Arduino Uno R3 board (Fig. 8).

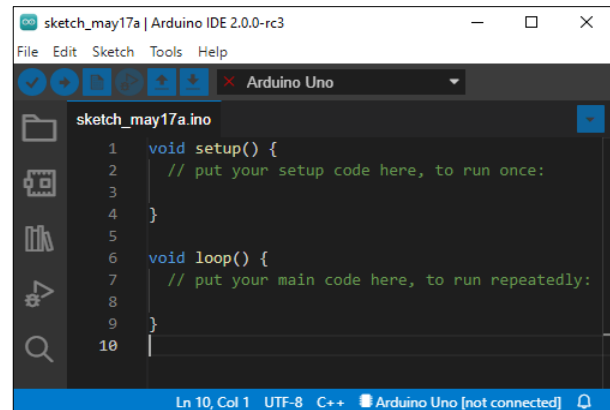


Fig. 8 Arduino IDE.

The three encryption algorithms (Speck, Present, and ISL-LWC) were implemented by the staff of the Information Security Laboratory of the Institute of Information and Computational Technologies of the Committee of Science of the Ministry of Science and Higher Education of the Republic of Kazakhstan (RK MSHE CS ICT ISL) in the high-level programming language C++.

The tests were carried out on open-source software platforms developed by the US National Institute of Standards and Technology in order to unify, simplify, and speed up the testing of lightweight cryptographic algorithms.

The results of the study and comparative analysis of the Present, Speck, and ISL-LWC algorithms are shown in Table V.

TABLE V COMPARATIVE ANALYSIS OF THE ALGORITHMS BY THE TIME OF ENCRYPTION AND KEY GENERATION

Encryption algorithm	Key size, bits	Plaintext block size, bits	Encryption time, μ s	Key setting time, μ s
Present	80	64	2111.56	1541.31
Speck	96		16.90	1320.69
ISL-LWC	80		108.59	275.12

As a result of a comparative analysis of Table V, it was found that the proposed encryption algorithm works faster than Present, and when scheduling round keys, it is 6 and 5 times faster than the algorithms under consideration, respectively.

V. CONCLUSION

Lightweight encryption algorithms are considered a relatively new direction in the development of symmetric cryptography. This need arose as a result of the emergence of a large number of devices with little computing power and memory. Therefore, there was a need to develop algorithms that can provide a sufficient level of security with minimal use of resources.

This paper provides a brief literature review of existing lightweight encryption algorithms. A new lightweight block encryption algorithm ISL-LWC, developed by the staff of the RK MSHE CS IICT ISL, is presented.

The cryptographic properties of the developed algorithm were studied using the evaluation of the "avalanche effect" and statistical tests. Based on the work carried out, it was found that the proposed encryption algorithm is effective in providing a good avalanche effect, and the encrypted data is close to random and is statistically safe.

The developed algorithm is implemented in software and hardware on the Arduino Uno R3 board. A study and comparative analysis of the encryption and key generation time with the well-known lightweight algorithms Present and Speck have been carried out.

The obtained test results allow us to conclude that the ISL-LWC cipher is generally not inferior to these two well-known lightweight algorithms. Further study of the cryptographic properties of this algorithm by other methods, such as linear and differential cryptanalysis, etc., will be continued. The results will be presented in subsequent papers and used to improve the proposed algorithm.

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